



W&B-INF-1852

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By: 

Date: June 29, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/626,957 Confirmation No. 3198
Applicant : Michael Sommer
Filed : July 25, 2003
TC/A.U. : 2822
Examiner : Ida M. Soward
Title: : Semiconductor Circuit Structure and Method
for Fabricating the Semiconductor Circuit
Structure

Docket No. : W&B-INF-1852
Customer No. : 24131

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

S i r :

In response to the Office action dated March 29, 2004, please
amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of
claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 20 of this paper.